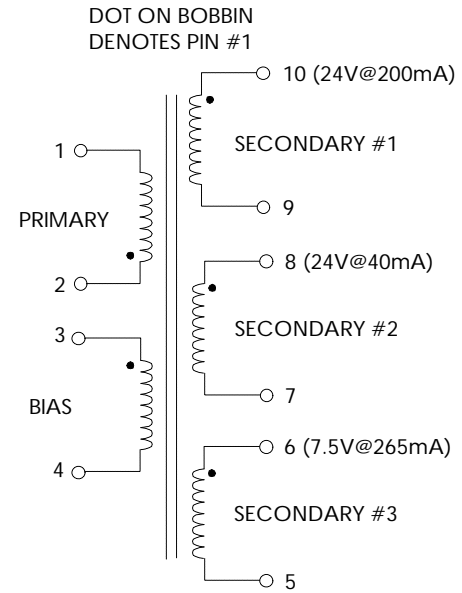


**TABLE 1: ELECTRICAL SPECIFICATIONS AT 25 °C**  
 SWITCHING TRANSFORMER DESIGNED FOR USE WITH TOP222  
 REFER TO APPLICATION CIRCUIT OF FIGURE 3

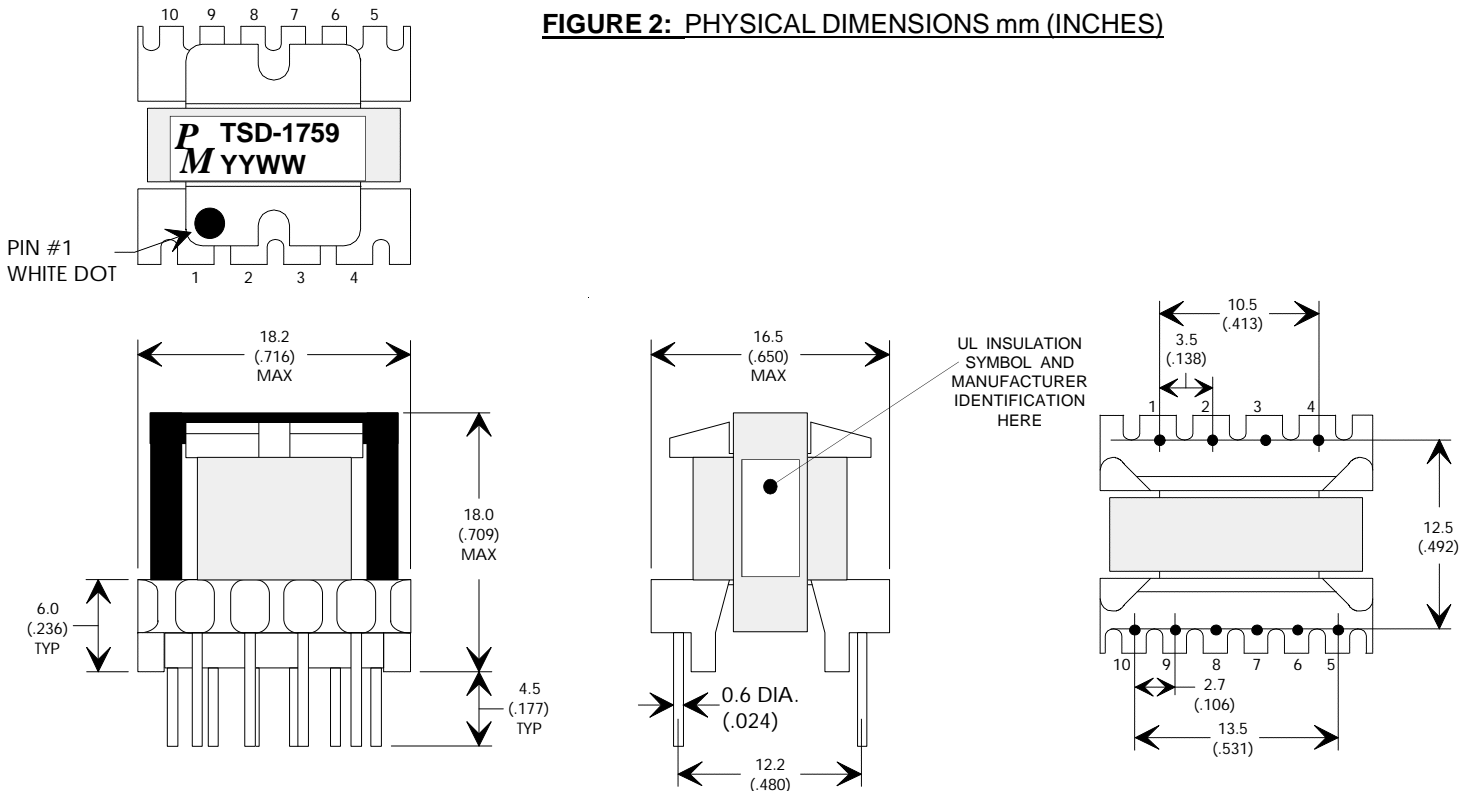
PARAMETER	SPEC LIMITS			UNITS
	MIN.	TYP.	MAX.	
PRIMARY INDUCTANCE (2-1) FREQ. = 100 KHZ @ 0.250Vrms	1400	1500	1600	μHY
TURNRATIO'S: SEC#1 (10-9) : PRIMARY (2-1) SEC#2 (8-7) : PRIMARY (2-1) SEC#3 (6-5) : PRIMARY (2-1) PRIMARY (2-1) : BIAS (3-4)	-----	1:3.10	-----	± 3%
	-----	1:3.10	-----	± 3%
	-----	1:9.07	-----	± 3%
	-----	1:5.9	-----	± 3%
PRILEAKAGE IND. (SEC'S SHORTED) FREQ. = 100 KHZ @ 0.250Vrms	-----	-----	75	μHY
HIPOT: PRIMARY TO SEC'S BIAS TO SEC'S	3000 3000	----- -----	----- -----	Vrms Vrms
APP CIRCUIT PARAMETERS: DC HOT RAIL VOLTAGE OUTPUT VOLTAGE (6-5) OUTPUT CURRENT CONTINUOUS OUTPUT VOLTAGE (10-9) OUTPUT CURRENT CONTINUOUS OUTPUT VOLTAGE (8-7) OUTPUT CURRENT CONTINUOUS LOAD REGULATION 10-100% RIPPLE	----- 0.0 0.0 0.0 0.0 ----- -----	115 7.5 ----- 24.0 ----- 24.0 ----- 10 50.0	----- 265 200 40 ----- -----	Vdc Vdc mA Vdc mA Vdc mA ±% ±mV

**FIGURE 1: SCHEMATIC DIAGRAM**



- NOTE1:**  
 A) ALL MATERIALS MEET "UL", "CSA" & "IEC" REQUIREMENTS  
 B) TRIPLE BASIC INSULATED SECONDARY.  
 C) DESIGNED TO MEET ≥6.2mm CREEPAGE REQUIREMENTS.  
 D) VARNISH FINISHED ASSEMBLY.

**FIGURE 2: PHYSICAL DIMENSIONS mm (INCHES)**



REV.	DESCRIPTION OF CHANGES	BY
06/05/02	ORIGINAL RELEASE	PP

EE16/EI16, 10-PIN VERTICAL



UNLESS OTHERWISE SPECIFIED  
 DIMENSIONS ARE IN MM  
 DIMENSIONAL TOLERANCES ARE:  
 DECIMALS ANGLES  
 .X ± .25 ±0° 30'  
 .XX ± .15  
 DO NOT SCALE DRAWING

**TRANSFORMER CONTROL DRAWING**

PREMIER P/N: TSD-1759	REVISION: 06/05/02
ENGR: PETER PHAM	REF:
SCALE: NONE	SHEET: 1 OF 4

